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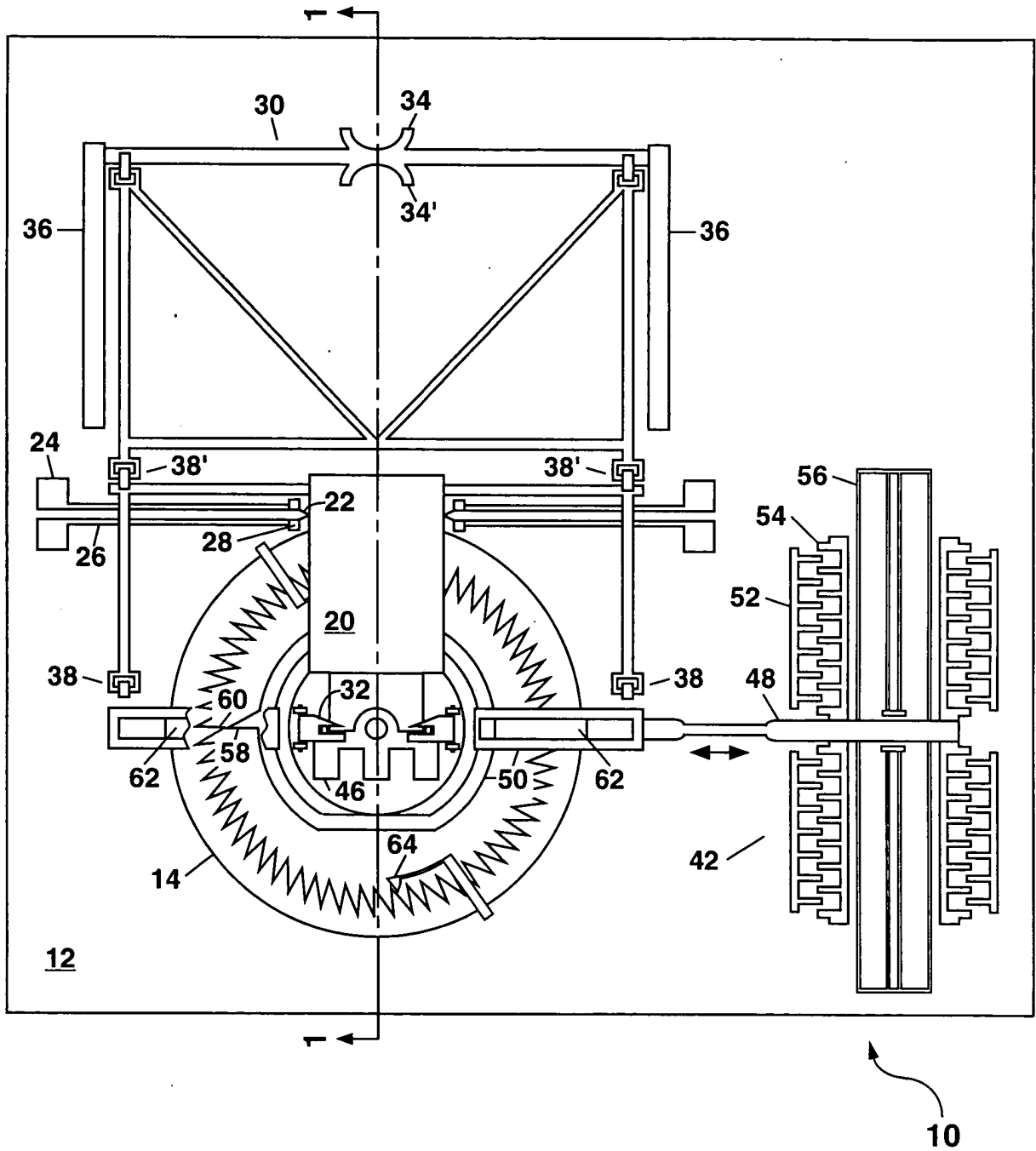
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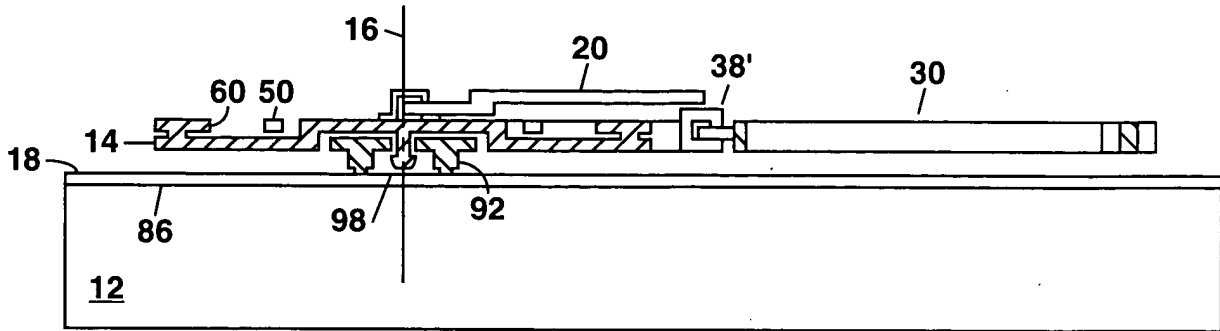


FIG. 2A

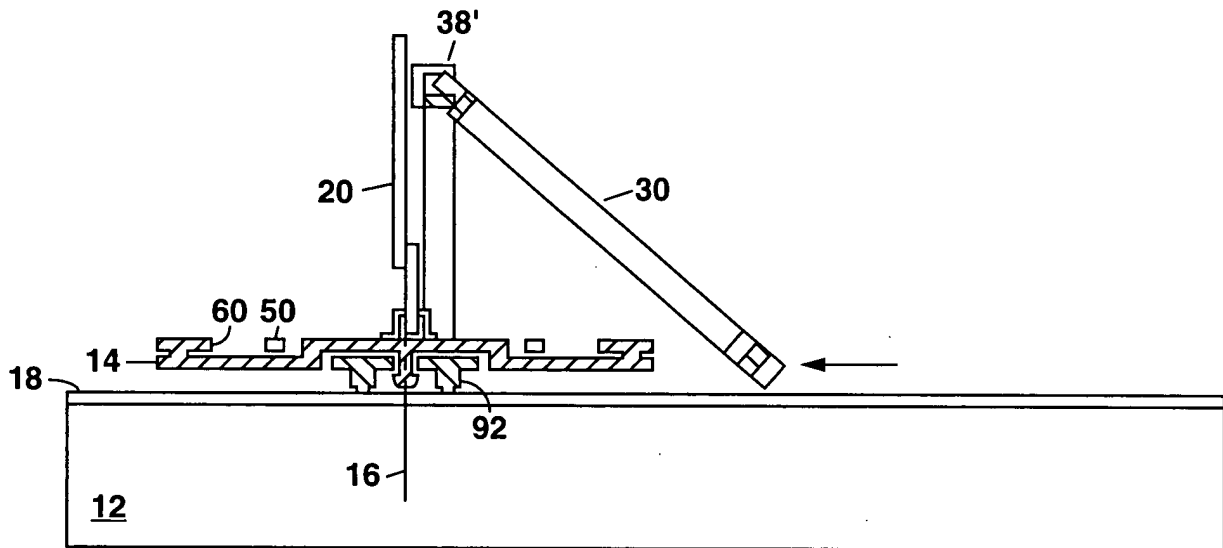


FIG. 2B

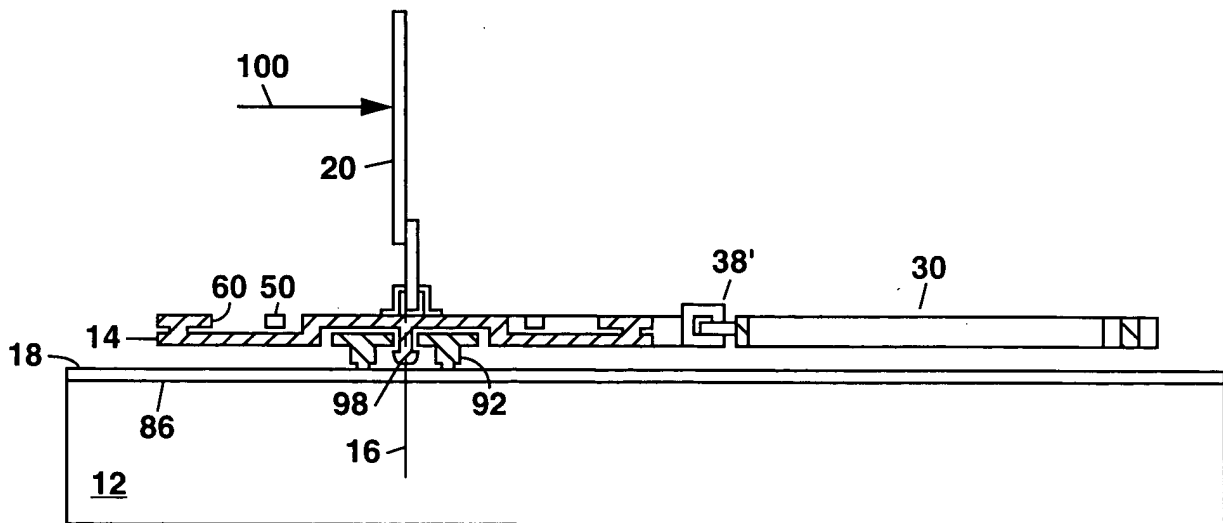


FIG. 2C

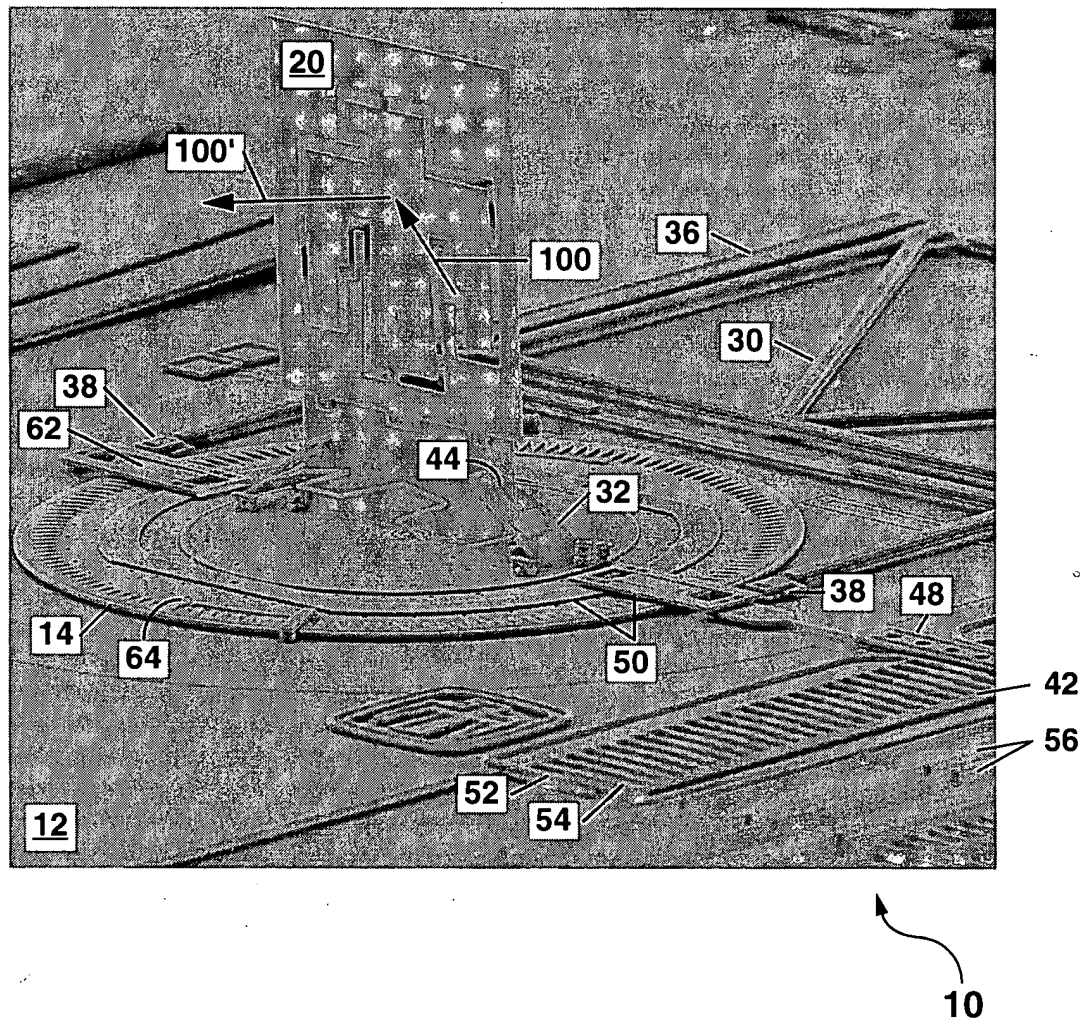


FIG. 3

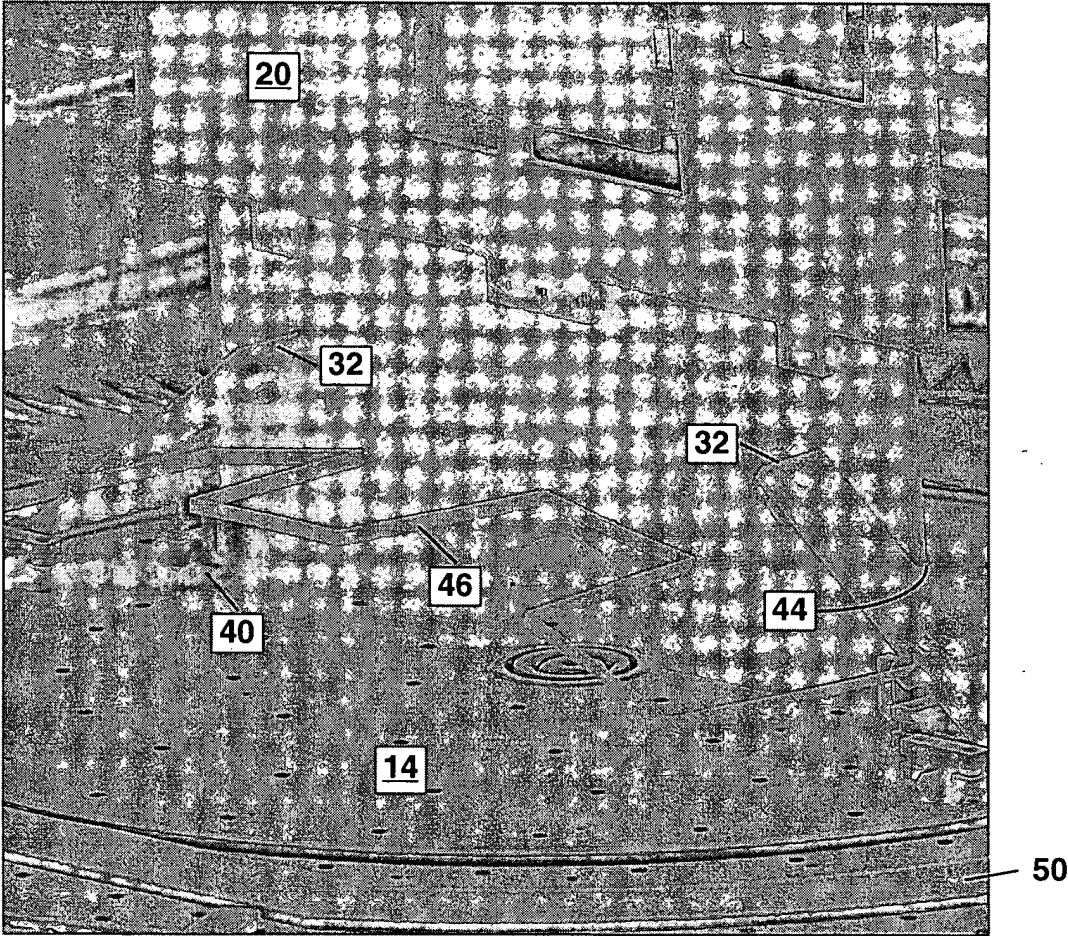
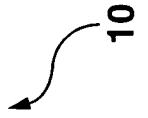


FIG. 4



12

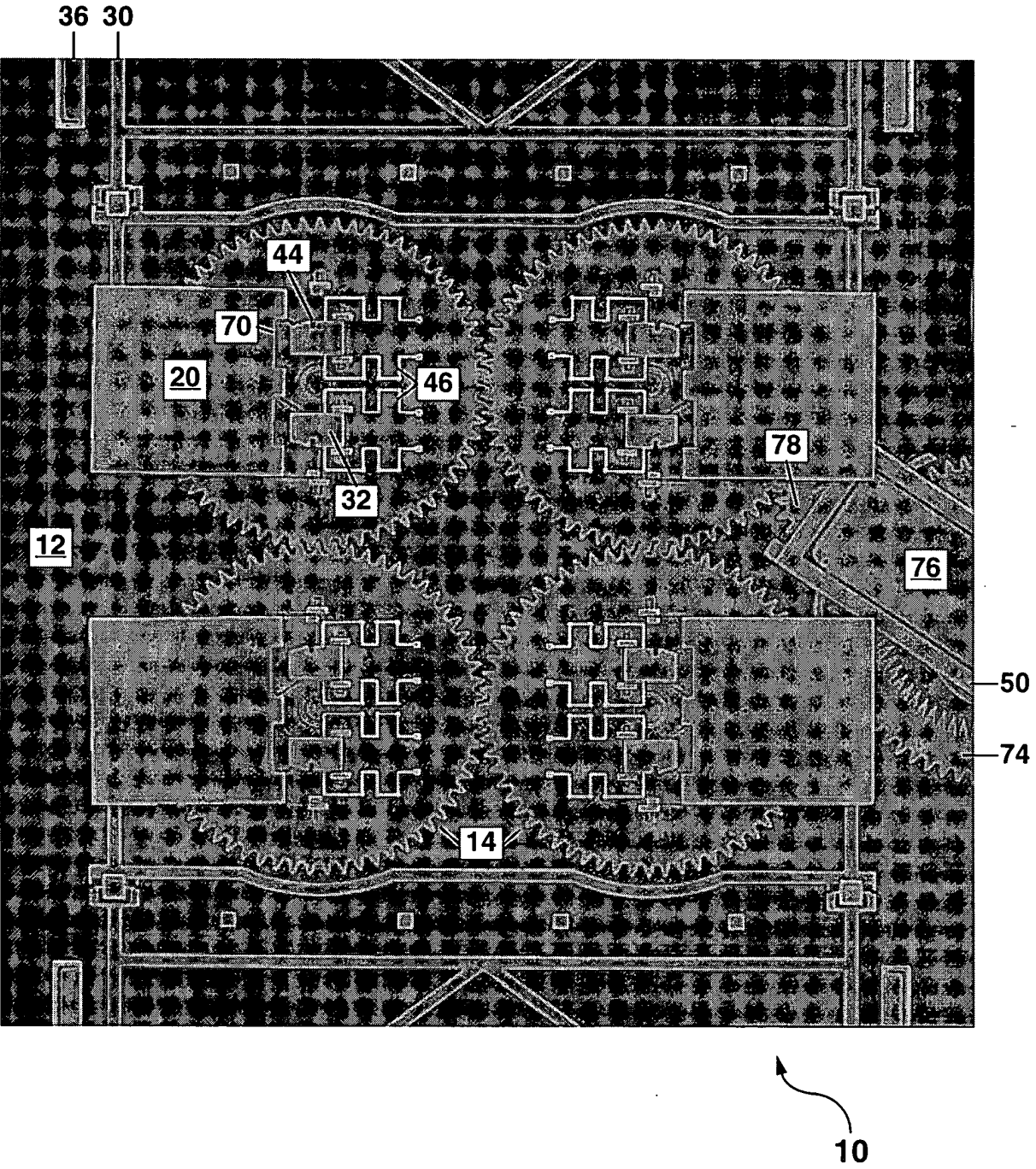


FIG. 6



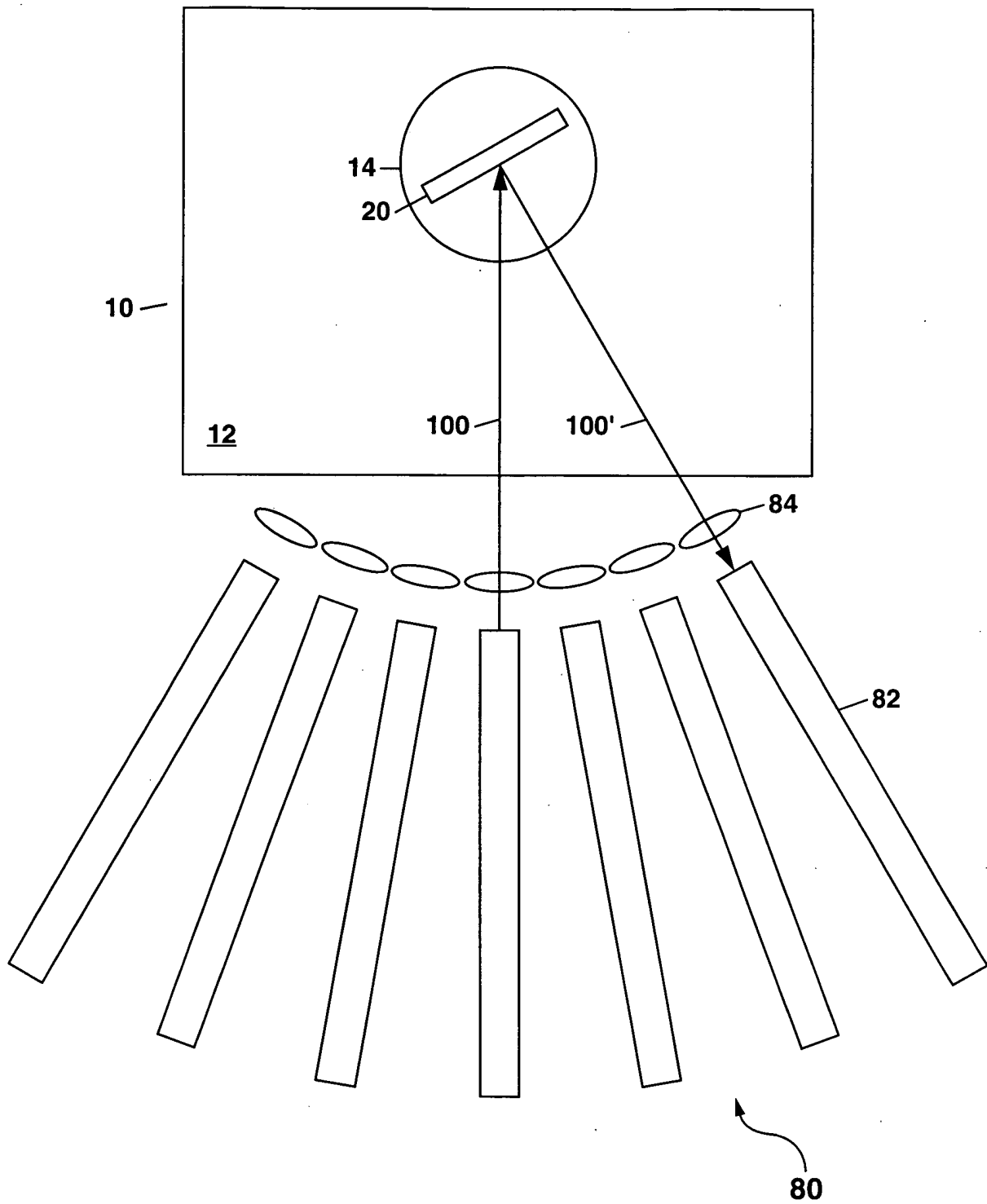


FIG. 7

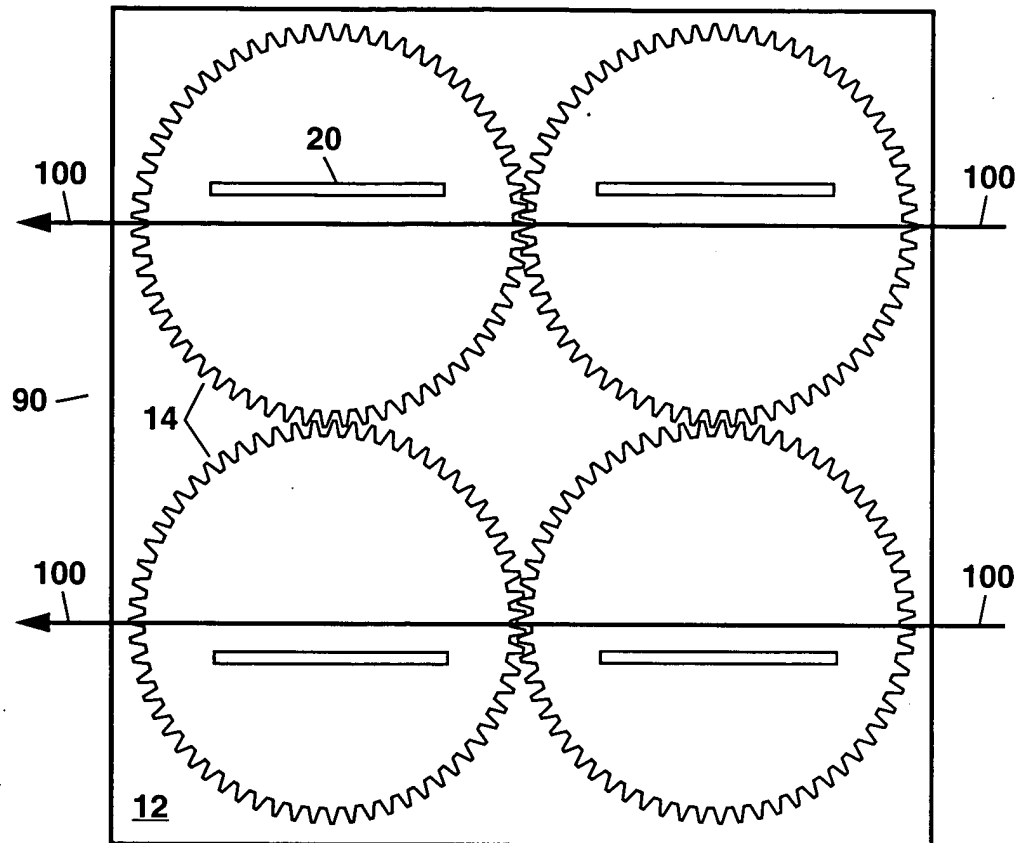


FIG. 8A

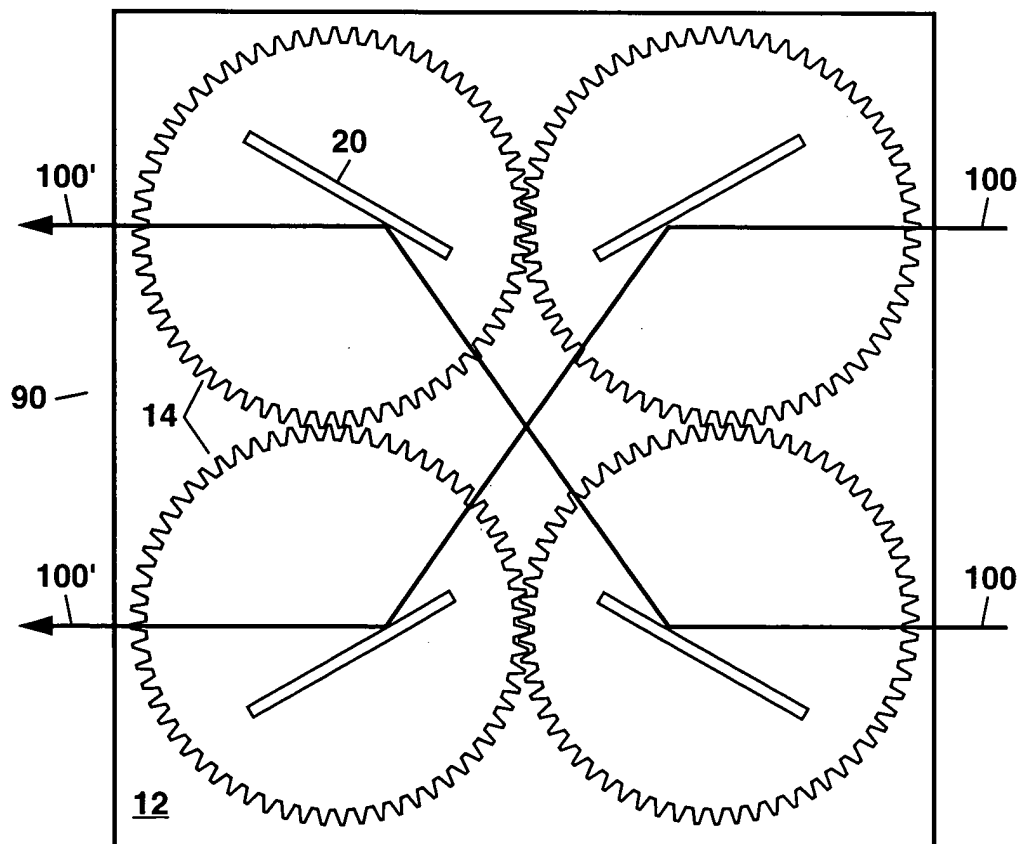


FIG. 8B

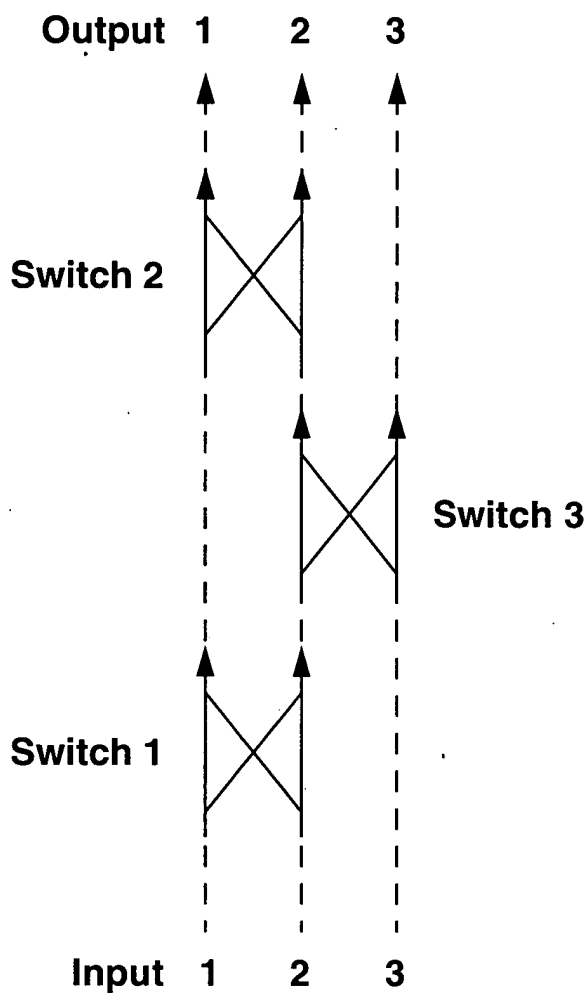


FIG. 9A

Switch 1	Switch 2	Switch 3	Output Signal Order
0	0	0	123
0	1	0	213
0	0	1	132
0	1	1	312
1	0	0	213
1	1	0	123
1	0	1	231
1	1	1	321

FIG. 9B



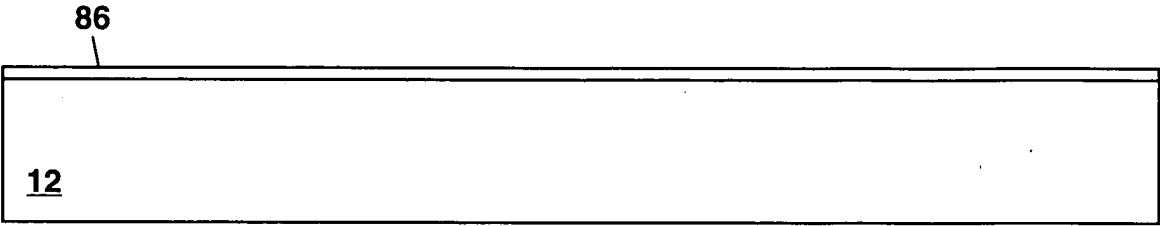


FIG. 10A

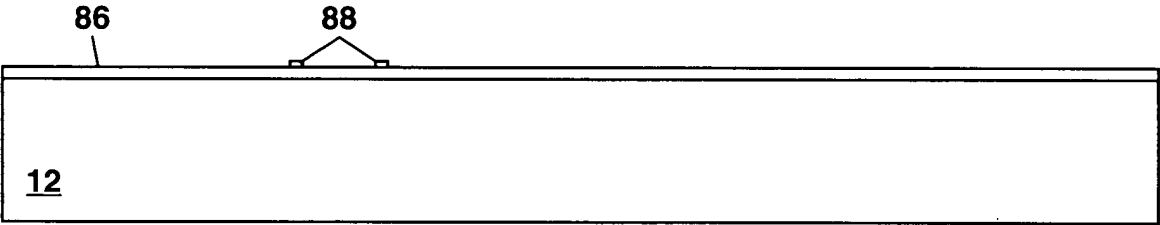


FIG. 10B

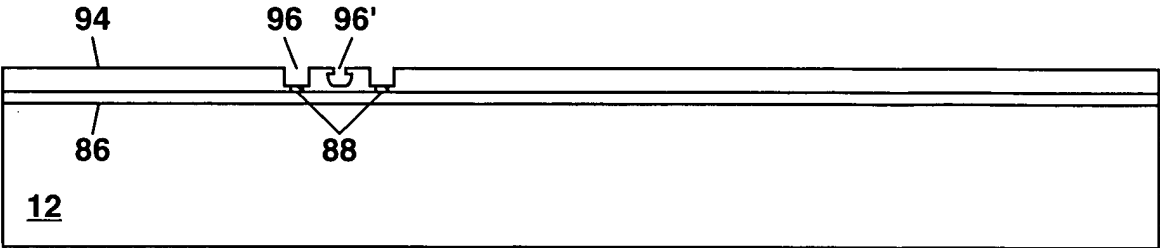


FIG. 10C



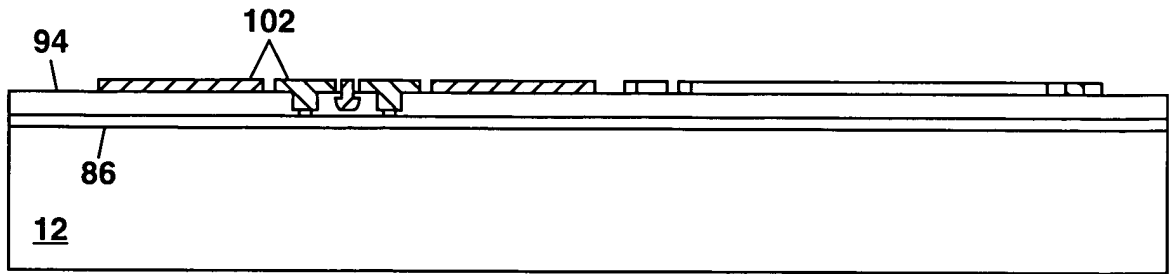


FIG. 10D

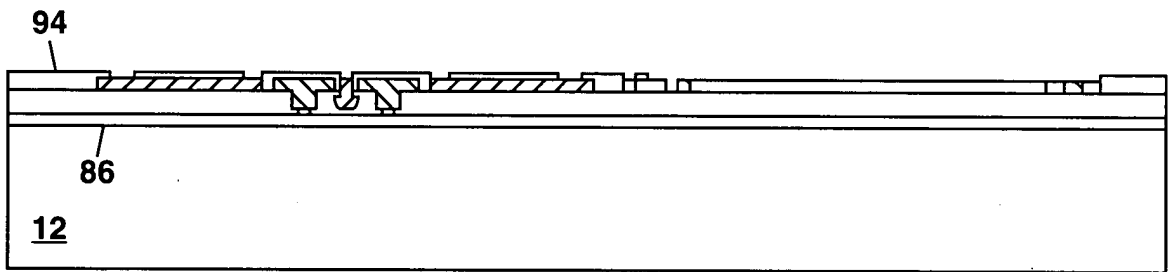


FIG. 10E

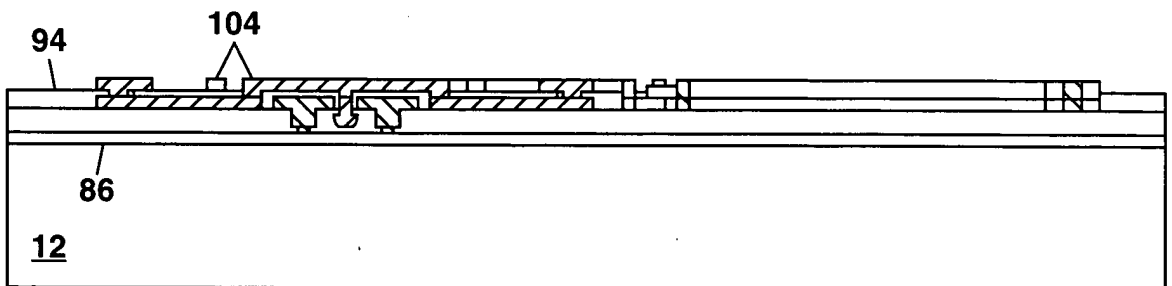


FIG. 10F



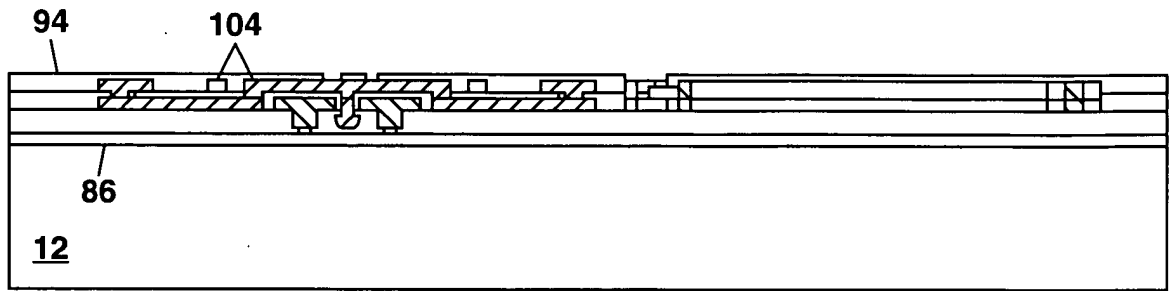


FIG. 10G

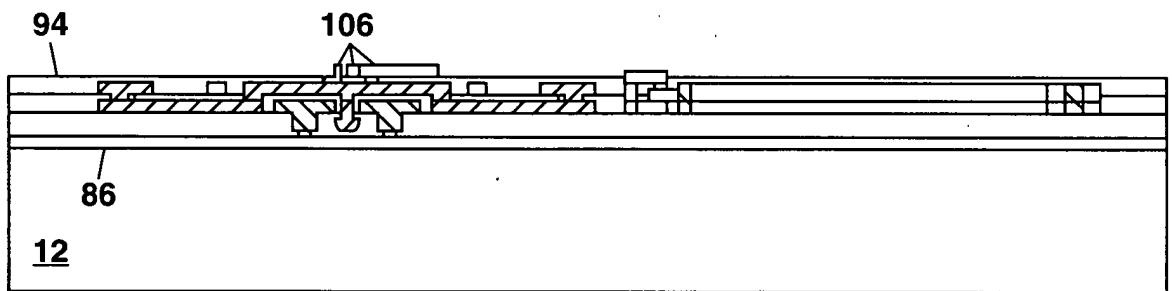


FIG. 10H

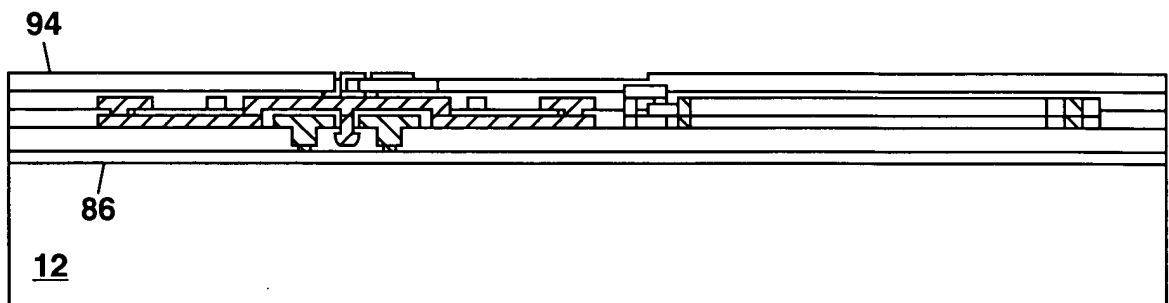


FIG. 10I



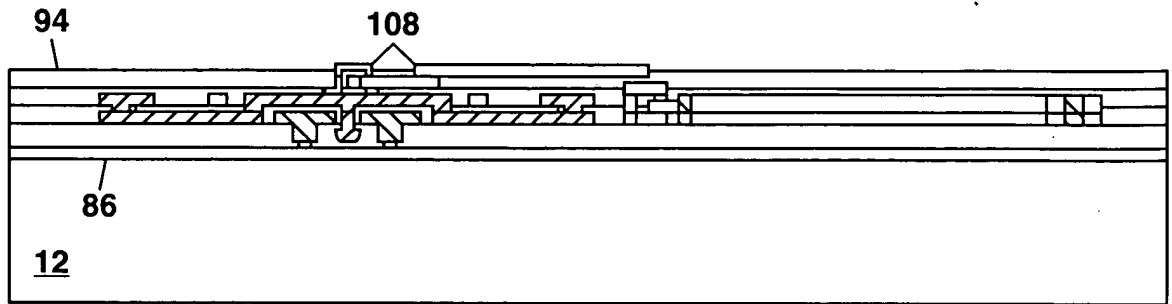


FIG. 10J

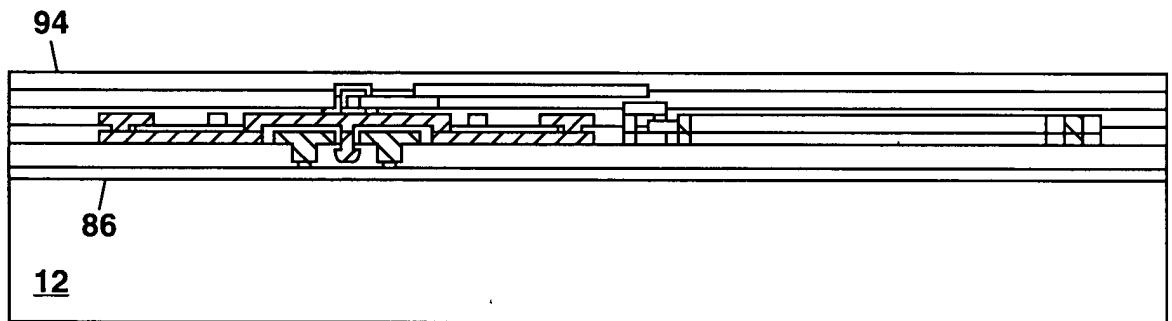


FIG. 10K

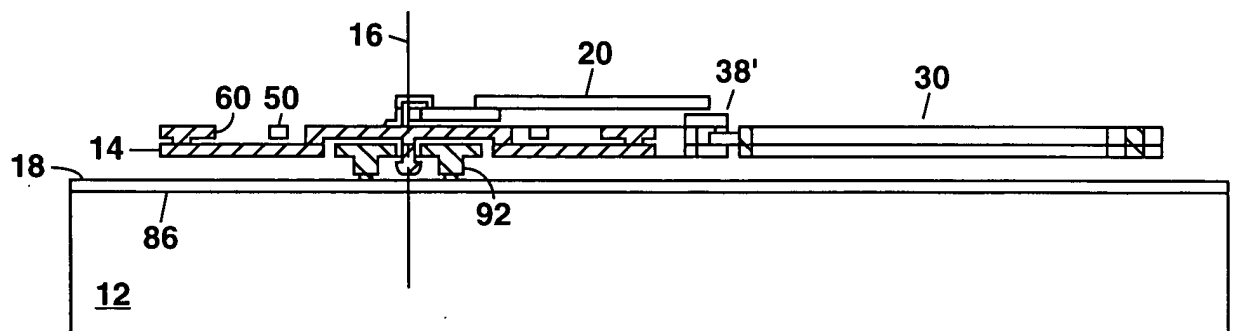


FIG. 10L